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1	esd and p-cell
2	esd and p-cell and @ad<"20031110"
3	esd and p-cell and schematic and grow\$6 and non adj grow\$4
4	esd and p-cell and schematic and grow\$4 and non adj grow\$5
5	(esd electro\$7 with discharg\$4) and cell
6	(esd electro\$7 with discharg\$4) and cell
7	(esd electro\$7 with discharg\$4) and cell and @ad<"20031110"
8	(esd electro\$7 with discharg\$4) and cell and @ad<"20031110" and ((higher lower) with level or hierarch\$6)
9	(esd electro\$7 with discharg\$4) same cell and @ad<"20031110" and ((higher lower) with level or hierarch\$6) and (grow\$5 expan\$6)
10	(esd electro\$7 with discharg\$4) same cell and @ad<"20031110" and ((higher lower) with level or hierarch\$6) and (grow\$5 expan\$6) and diode
11	(esd electro\$7 with discharg\$4) same cell and @ad<"20031110" and ((higher lower) with level or hierarch\$6) and (grow\$5 expan\$6) and diode same (esd electro\$7 with discharg\$4 protect\$4 clamp\$4 overvoltage over adj voltage)
12	(esd electro\$7 with discharg\$4) and "716"/\$.ccls.
13	(esd electro\$7 with discharg\$4) and "716"/\$.ccls. and cell and diode same (string esd electro\$7 with discharg\$4 protect\$4 clamp\$4 overvoltage over adj voltage)
14	(esd electro\$7 with discharg\$4) and "716"/\$.ccls. and cell and diode same (string esd electro\$7 with discharg\$4 protect\$4 clamp\$4 overvoltage over adj voltage) and ((higher lower) with level or hierarch\$6)
15	(esd electro\$7 with discharg\$4) and "716"/\$.ccls. and cell and diode same (string esd electro\$7 with discharg\$4 protect\$4 clamp\$4 overvoltage over adj voltage) and ((higher lower) with level or hierarch\$6) and library
16	(esd electro\$7 with discharg\$4) and "716"/\$.ccls. and cell and (grow\$6 expan\$6 increas\$4) and (fixed)
17	(esd electro\$7 with discharg\$4) and "716"/\$.ccls. and cell and (grow\$6 expan\$6 increas\$4) and (fixed) and schematic\$5
18	(esd electro\$7 with discharg\$4) and "716"/\$.ccls. and cell and (grow\$6 expan\$6 increas\$4) and (fixed) and schematic\$5 and high\$4 and low
19	(esd electro\$7 with discharg\$4) and "716"/\$.ccls. and cell and (grow\$6 expan\$6 increas\$4) and (fixed) and schematic\$5 and high\$4 and low\$3
20	(esd electro\$7 with discharg\$4) and "716"/\$.ccls. and cell and (grow\$6 expan\$6 increas\$4) and (fixed) and schematic\$5 and (high\$4 top\$6 upper\$6 root\$4) same (grow\$6 expan\$6 increas\$4) same (fixed)